

17638 123103
U.S. PTO

22386 U.S. PTO
10/750389

123103

By Express Mail #EV410260598US

Attorney Docket No.: 5367-65

Check box if applicable: DUPLICATE

UTILITY PATENT APPLICATION TRANSMITTAL

Submit an original and a duplicate for fee processing

(Only for new nonprovisional applications under 37 C.F.R. § 1.53(b))

Dated: December 31, 2003

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Transmitted herewith for filing is the utility patent application of:

Inventor(s): Tony ALBRECHT, Wilhelm STEIN, Ralph WIRTH

For: Light Emitting Diode Chip with Radiation-Transmissive Electrical Current Expansion Layer

Enclosed are:

- Transmittal letter (2x) with Fee Computation Sheet
- General Authorization For Payment of Fees (2x)
- Specification, Claims 1 to 21 & Abstract (20 pages)
- One (1) sheet of drawing(s) (Figs. 1 to 2)
- Check for \$788 for filing fee
- Information Disclosure Statement
- PTO Form SB/08A with copies of cited references
- Return Receipt Postcard

This application is to be assigned to: Osram Opto Semiconductors GmbH
Wernerwerkstrasse 2
93049 Regensburg, Germany

By Express Mail #EV410260598US

- Please charge my Deposit Account No. 03-2412 in the amount of \$. A duplicate copy of this sheet is enclosed.
- The Commissioner is hereby authorized to charge payment of the following fees associated with this application or credit any overpayment to Deposit Acct. No. 03-2412.
 - Any additional filing fees required under 37 CFR 1.16.
 - Any patent application processing fees under 37 CFR 1.17
 - Any filing fees under 37 CFR 1.16 for presentation of extra claims.
- Priority is claimed for this invention and application, corresponding applications having been filed in **Germany** on **December 31, 2002**, No. **102 61 676.0**.

Respectfully submitted,
COHEN, PONTANI, LIEBERMAN & PAVANE

By: Thomas Langer
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Reg. No. 27,264

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FILING FEE COMPUTATION SHEET

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In re Application of: Tony ALBRECHT et al.

For: Light Emitting Diode Chip with Radiation-Transmissive Electrical Current Expansion Layer

The filing fee has been calculated as shown below:

FOR:	Col. 1	Col. 2	SMALL ENTITY	OTHER THAN SMALL ENTITY
	# FILED	# EXTRA		
BASIC FEE			\$385	\$770
TOTAL CLAIMS	<u>21</u> - 20 =	<u>1</u>	x 9 = \$	x 18 = \$ 18
INDEPENDENT CLAIMS	<u>1</u> - 3 =	<u>0</u>	x 43 = \$	x 86 = \$
<input type="checkbox"/> MULTIPLE DEPENDENCY			+\$145 = \$	+290 \$
* If the difference in Col. 1 is less than zero, enter "0" in Col. 2			TOTAL: \$	\$788

Attorney Docket # 5367-65

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Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Tony ALBRECHT et al.

Serial No.: n/a

Filed: concurrently

For: Light Emitting Diode Chip with
Radiation-Transmissive Electrical
Current Expansion Layer

Check box if applicable:

DUPLICATE

**GENERAL AUTHORIZATION FOR PAYMENT OF FEES
AND PETITIONS FOR EXTENSIONS OF TIME**

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Mail Stop Patent Application

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

The Commissioner is hereby authorized to credit overpayments or charge the following fees to Deposit Account No. 03-2412

- Any filing fees required under 37 CFR §1.16.
- Any patent application processing fees under 37 CFR §1.17 not otherwise paid by check.
- The issue fee set in 37 CFR 1.18 at 3 months from mailing of the Notice of Allowance, pursuant to 37 CFR 1.311 (b) provided the fee has not already been paid by check.
- Any filing fees under 37 CFR 1.16 for presentation of extra claims.

Respectfully submitted,
COHEN, PONTANI, LIEBERMAN & PAVANE

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